A1063

UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Cheng H. Huang et al.

Application No. : 10/646,013

Confirmation No. : 8979

Filed : August 22, 2003

For : ELECTRICALLY-PROGRAMMABLE INTEGRATED

CIRCUIT ANTIFUSES

Group Art Unit : 2826

Examiner : Remmon R. Forde

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, applicants hereby bring the attention of the Examiner to the documents listed on the attached Form PTO-1449 (submitted in duplicate).

A copy of each listed document that is not a U.S. patent is enclosed herewith.

Respectfully Submitted,

G. Victor Treyz/, Reg. No. 36,294

Attorney for Applicants

Customer No. 36532

Substitute for form 1449A/PTO

PTO/SB/08a (08-03) Approved for use through 07/31/2006. OMB 0651-0031 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known **Application Number** 10/646,013 Filing Date 8/22/2003 First Named Inventor Cheng H. Huang Art Unit 2826 **Examiner Name** Remmon R. Forde

(Use as many sheets as necessary)

Attorney Docket Number A1063

	U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ^{2 (Finown)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		^{US-} 4,207556	06-10-1980	Yoshi et al.		
		^{US-} 4,433,331	02-21-1984	Kollaritsch		
		^{US-} 4,609,986	02-21-1986	Hartmann et al.		
	<u> </u>	us- 4.617.479	10-14-1986	Hartmann et al.		
		4,761,768	08-02-1988	Turner et al.		
	<u> </u>	^{US-} 4.642.487	02-10-1987	Carter		
	İ	^{US-} 5,510,730	04-23-1996	El Gamal et al.		
		^{US-} 5,291,434	03-01-1994	Kowalski		
		^{US-} 6,477,094	11-05-2002	Kim et al.		
		^{US-} 6,456,546	09-24-2002	Kim et al.		
n ngay i - Mininggapapatan ng a ngalagan ngalagan		^{US-} 5,303,199	04-12-1994	Ishihara et al.		
		^{US-} 5,070,384	12-03-1991	McCollum et al.		
		^{US-} 5,463,244	10-31-1995	De Araujo et al.		
		^{US-} 6,125,069	09-26-2000	Aoki		
		^{US-} 6,108,261	08-22-2000	Kim et al.		
		^{US-} 5,844,298	12-01-1998	Smith et al.		
	<u> </u>	US- 5,831,923	11-03-1998	Casper		
		US- _{10/780,427}	N/A	Chih-Ching Shih et al.		

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)	MM-DD-YYYY			Ta
***************************************					***************************************	
					14 ******	\Box
					***************************************	П

Date
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

Traisation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commence, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



PTO/SB/08b (08-03)
Approved for use through 06/30/2006. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Lot of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449B/PTO Complete if Known Application Number 10/646,013 Filing Date INFORMATION DISCLOSURE 8/22/2003 First Named Inventor STATEMENT BY APPLICANT Cheng H. Huang Art Unit 2826 (Use as many sheets as necessary) Examiner Name Remmon R. Forde Sheet 2 Attorney Docket Number 2 A1063

		NON PATENT LITERATURE DOCUMENTS	NON PATENT LITERATURE DOCUMENTS				
Examiner Initials* Cite No.1 Include name of the author (in CAPITAL LETTERS), title of the article (who magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume and/or country where published.		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²				
		WEI ZHANG ET AL., "Energy Effect of the Laser-Induced Vertical					
		Metallic Link", IEEE Transactions on Semiconductor Manufacturing, Vol 14, No. 2, May 2001 pp 163-169					
		"Analyzing the process window for laser copper-link processing"	·				
***************************************		Solid State Technology - Semiconductor manufacturing and wafer fabrication, Jan 8, 2003					
		ALEXANDER KALNITSKY et al. "CoSi2 integrated fuses on poly silicon for low voltage 0.18 um CMOS applications" (c) 1999 IEEE					
		MOHSEN ALAVI "A PROM Element Based on Salicide Agglomeration of Poly Fuses in a CMOS logic process" IEEE International Electron Devices Meeting, December 1997					
		NORIAKI SATO et al. "A New Programmable Cell Utilizing Insulator Breakdown", IEDM 1985, pp 639-642					
		JINBONG KIM et al. "Three-Transistor One-Time Programmable (OTP) ROM Cell Array Using Standard CMOS Gate Oxide Antifuse", IEEE Electron Device Letters, Vol 24, No. 9, September 2003 pp. 589-591					
		WEI ZHANG et al. "Laser-Formed Vertical Metallic Link and Potential Implementation in Digital Logic Integration", PROC MAPLD, MD, 1999 pp 1-7 (B5)					
			ļ				

$\overline{}$	<u> </u>	
Examine	Date	
Signature	Considered	

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.